packets onto said interconnect if it is determined that information on the interconnect matches said one or more conditions.

16(Twice Amended). A functional circuit comprising: an interconnect;

10      one or more modules connected to the interconnect; and

a monitoring circuit for monitoring information packets put onto the interconnect by one or more modules, <u>said information packets including packet routing information and</u> said monitoring circuit comprising:

circuitry for determining if the information packet on the interconnect

15      matches one or more conditions; and

circuitry for preventing a module from putting further information onto said interconnect if it is determined that information packet on the interconnect matches said one or more conditions.

24(Twice Amended). A circuit comprising:

an interconnect;

one or more modules connected to the interconnect to put information packets onto the interconnect, wherein the information packets comprise

5      packet routing information;

an arbiter for determining which module is permitted to put information packets onto the interconnect; and

circuitry for preventing a module from putting further information packets onto said interconnect, said preventing circuitry preventing a module

10      from winning an arbitration carried out by said arbiter.

25(Twice Amended). A method comprising the steps of:

monitoring information packets on an interconnect, the information comprising packet routing information and being put onto the interconnect by one or modules; determining if the information on an interconnect satisfies one or more conditions; and

preventing a module from putting information packets onto an interconnect if it is determined that the information satisfies one or more conditions.

26(NEW). A circuit for monitoring packet information put onto an interconnect by one or more modules, wherein said interconnect is not a

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